1.2V Reference

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INTRODUCTION

Temperature compensated zener diodes are the most easily used voltage reference. However, the lowest voltage temperature-compensated zener is 6.2V. This makes it inconvenient to obtain a zero temperature-coefficient reference when the operating supply voltage is 6V or lower. With the availability of the LM113, this problem no longer exists.

The LM113 is a 1.2V temperature compensated shunt regulator diode. The reference is synthesized using transistors and resistors rather than a breakdown mechanism. It provides extremely tight regulation over a wide range of operating currents in addition to unusually low breakdown voltage and low temperature coefficient.

DESIGN CONCEPTS

The reference in the LM113 is developed from the highlypredictable emitter-base voltage of integrated transistors. In its simplest form, the voltage is equal to the energy-bandgap voltage of the semiconductor material. For silicon, this is 1.205V. Further, the output voltage is well determined in a production environment.

A simplified version of this reference¹ is shown in *Figure 1*. In this circuit, Q₁ is operated at a relatively high current density. The current density of Q₂ is about ten times lower, and the emitter-base voltage differential (ΔV_{BE}) between the two devices appears across R₃. If the transistors have high current gains, the voltage across R₂ will also be proportional to ΔV_{BE} . Q₃ is a gain stage that will regulate the drop across R₂. The emitter base voltage of Q₃ has a negative temperature coefficient while the ΔV_{BE} component across R₂ has a positive temperature coefficient. It will be shown that the output voltage will be temperature compensated when the sum of the two voltages is equal to the energy-band-gap voltage.



Conditions for temperature compensation can be derived starting with the equation for the emitter-base voltage of a transistor which is^2

$$V_{BE} = V_{g0} \left(1 - \frac{T}{T_0} \right) + V_{BE0} \left(\frac{T}{T_0} \right) + \frac{nkT}{q} \log_e \frac{T_0}{T} + \frac{kT}{q} \log_e \frac{l_c}{l_{C0}}, \qquad (1)$$

where V_{g0} is the extrapolated energy-band-gap voltage for the semiconductor material at absolute zero, q is the charge of an electron, n is a constant which depends on how the transistor is made (approximately 1.5 for double-diffused, NPN transistors), k is Boltzmann's constant, T is absolute temperature, I_C is collector current and V_{BE0} is the emitterbase voltage at T₀ and I_{C0}.

The emitter-base voltage differential between two transistors operated at different current densities is given by

$$\Delta V_{BE} = \frac{k I}{q} \log_e \frac{J_1}{J_2}$$
(2)

where J is current density.

Referring to Equation (1), the last two terms are quite small and are made even smaller by making I_C vary as absolute temperature. At any rate, they can be ignored for now because they are of the same order as errors caused by nontheoretical behavior of the transistors that must be determined empirically.

If the reference is composed of V_{BE} plus a voltage proportional to ΔV_{BE} , the output voltage is obtained by adding (1) in its simplified form to (2):

$$V_{ref} = V_{g0} \left(1 - \frac{T}{T_0} \right) + V_{BE0} \left(\frac{T}{T_0} \right) + \frac{kT}{q} \log_e \frac{J_1}{J_2}.$$
 (3)

Differentiating with respect to temperature yields

$$\frac{\partial V_{ref}}{\partial T} = -\frac{V_{g0}}{T_0} + \frac{V_{BE0}}{T_0} + \frac{k}{q} \log_e \frac{J_1}{J_2}.$$
 (4)

For zero temperature drift, this quantity should equal zero, giving

$$V_{g0} = V_{BE0} + \frac{kT_0}{q} log_e \frac{J_1}{J_2}. \tag{5}$$

The first term on the right is the initial emitter-base voltage while the second is the component proportional to emitterbase voltage differential. Hence, if the sum of the two are equal to the energy-band-gap voltage of the semiconductor, the reference will be temperature-compensated.

Figure 2 shows the actual circuit of the LM113. Q_1 and Q_2 provide the ΔV_{BE} term and Q_4 provides the V_{BE} term as in the simplified circuit. The additional transistors are used to decrease the dynamic resistance, improving the regulation of the reference against current changes. Q_3 in conjunction with current inverter, Q_5 and Q_6 , provide a current source load for Q_4 to achieve high gain.

 Q_7 and Q_9 buffer Q_4 against changes in operating current and give the reference a very low output resistance. Q_8 sets the minimum operating current of Q_7 and absorbs any leak-

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REVERSE VOLTAGE (V)

APPLICATION

REVERSE CURRENT (A) -01 -01 -01

10⁻⁵ 12 1.4 1.6 0.8 1.0 1.2 1.4

125° 0

The applications for zener diodes are so numerous that no attempt to delineate them will be made. However, the low

breakdown voltage and the fact that the breakdown voltage is equal to a physical property of silicon—the energy band gap voltage—makes it useful in several interesting applications. Also the low temperature coefficient makes it useful in regulator applications—especially in battery powered systems where the input voltage is less than 6V.

Figure 6 shows a 2V voltage regulation which will operate on input voltages of only 3V. An LM113 is the voltage reference and is driven by a FET current source, Q₁. An operational amplifier compares a fraction of the output voltage with the reference. Drive is supplied to output transistor Q₂ through the V⁺ power lead of the operational amplifier. Pin 6 of the op amp is connected to the LM113 rather than the output since this allows a lower minimum input voltage. The dynamic resistance of the LM113 is so low that current changes from the output of the operational amplifier do not appreciably affect regulation. Frequency compensation is accomplished with both the 50 pF and the 1 µF output capacitor.





It is important to use an operational amplifier with low quiescent current such as an LM108. The quiescent current flows through R_2 and tends to turn on Q_2 . However, the value shown is low enough to insure that Q_2 can be turned off at worst case condition of no load and 125°C operation.

Figure 7 shows a differential amplifier with the current source biased by an LM113. Since the LM113 supplies a reference voltage equal to the energy band gap of silicon, the output current of the 2N2222 will vary as absolute temperature. This compensates the temperature sensitivity of the transconductance of the differential amplifier making the gain temperature stable. Further, the operating current is





regulated against supply variations keeping the gain stable over a wide supply range.

As shown, the gain will change less than two per cent over a -55° C to $+125^{\circ}$ C temperature range. Using the LM114A monolithic transistor and low drift metal film resistors, the amplifier will have less than 2 μ V/°C voltage drift. Even lower drift may be obtained by unbalancing the collector load resistors to null out the initial offset. Drift under nulled condition will be typically less than 0.5 μ V/°C.

The differential amplifier may be used as a pre-amplifier for a low-cost operational amplifier such as an LM101A to improve its voltage drift characteristics. Since the gain of the operational amplifier is increased by a factor of 100, the frequency compensation capacitor must also be increased from 30 pF to 3000 pF for unity gain operation. To realize low voltage drift, case must be taken to minimize thermoelectric potentials due to temperature gradients. For example, the thermoelectric potential of some resistors may be more than 30 μ V/°C, so a 1°C temperature gradient across the resistor on a circuit board will cause much larger errors than the amplifier drift alone. Wirewound resistors such as Evenohm are a good choice for low thermoelectric potential.

Figure 8 illustrates an electronic thermometer using an inexpensive silicon transistor as the temperature sensor. It can provide better than 1°C accuracy over a 100°C range. The emitter-base turn-on voltage of silicon transistors is linear with temperature. If the operating current of the sensing transistor is made proportional to absolute temperature the nonlinearily of emitter-base voltage can be minimized. Over a -55° C to 125° C temperature range the nonlinearily is less than 2 mV or the equivalent of 1°C temperature change.

An LM113 diode regulates the input voltage to 1.2V. The 1.2V is applied through R_2 to set the operating current of the temperature-sensing transistor.

Resistor R_4 biases the output of the amplifier for zero output at 0°C. Feedback resistor R_5 is then used to calibrate the output scale factor to 100 mV/°C. Once the output is zeroed, adjusting the scale factor does not change the zero.



CONCLUSION

A new two terminal low voltage shunt regulator has been described. It is electrically equivalent to a temperature-stable 1.2V breakdown diode. Over a $-55^\circ\mathrm{C}$ to $125^\circ\mathrm{C}$ temperature range and operating currents of 0.5 mA to 20 mA the LM113 has one hundred times better reverse characteristics than breakdown diode. Addiitionally, wideband noise and long term stability are good since no breakdown mechanism is involved.

The low temperature coefficient and low regulation voltage make it especially suitable for a low voltage regulator or battery operated equipment. Circuit design is eased by the fact that the output voltage and temperature coefficient are largely independent of operating current. Since the reference voltage is equal to the extrapolated energy-band-gap of silicon, the device is useful in many temperature compensation and temperature measurement applications.

REFERENCES

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2. J.S. Brugler, "Silicon Transistor Biasing for Linear Collector Current Temperature Dependence," IEEE Journal of Solid State Circuits, pp. 57–58, June, 1967.

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