



ICP-100-2 CN1		M30624FC(L)FP U1	
PIN89	89	P107/AN7/KI3	P47/CS3*
PIN90	90	P106/AN6/KI2	P46/CS2*
PIN91	91	P105/AN5/KI1	P45/CS1*
PIN92	92	P104/AN4/KI0	P44/CS0*
PIN93	93	P103/AN3	P43/A19
PIN94	94	P102/AN2	P42/A18
PIN95	95	P101/AN1	P41/A17
PIN97	97	P100/AN0	P40/A16
PIN99	99	AVCC	P37/A15
PIN98	98	VREF	P36/A14
PIN96	96	AVSS	P33/A11
PIN100	100	P97/ADTRG*	P30/AB(/-/D7)
PIN1	1	P96/ANEX1	P27/A7(/D7/D6)
PIN2	2	P95/ANEX0	P26/A6(/D6/D5)
PIN3	3	P94/DA1	P25/A5(/D5/D4)
PIN4	4	P93/DA0	P24/A4(/D4/D3)
PIN5	5	P92/TB2IN	P23/A3(/D3/D2)
PIN6	6	P91/TB1IN	P22/A2(/D2/D1)
PIN7	7	P90/TB0IN	P21/A1(/D1/D0)
PIN8	8	BYTE	P20/A0(/D0/-)
PIN9	9	CNVSS	
PIN12	12	RESET*	P57/RDY*/CLKOUT
15	15	XIN	P56/ALE
13	13	XOUT	P55/HOLD*
PIN10	10	P87/XCIN	P54/HLDA*
PIN11	11	P86/XCOUT	P53/BCLK
PIN17	17	P85/NMI*	P52/RD*
PIN18	18	P84/INT2*	P51/WRH*/BHE*
PIN19	19	P83/INT1*	P50/WRL*/WR*
PIN20	20	P82/INT0*	
PIN21	21	P81/TA4IN	
PIN22	22	P80/TA4OUT	
PIN31	31	P67/TXD1	P07/D7
PIN32	32	P66/RXD1	P06/D6
PIN33	33	P65/CLK1	P05/D5
PIN34	34	P64/CTS1/RTS1	P04/D4
PIN35	35	P63/TXD0	P03/D3
PIN36	36	P62/RXD0	P02/D2
PIN37	37	P61/CLK0	P01/D1
PIN38	38	P60/CTS0/RTS0	P00/D0
16	16	VCC	
62	62	VCC	
14	14	VSS	
64	64	VSS	

SCALE		DATE		M16C62-FLASH-CPU-ICE-BOARD	
		'98.12.B		MSA0652 VER 1.3A	
Page 1/1					
DRAWN	CHECKED	DESIGNED	APPROVED		